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				Application Number	10/038,473
				Filing Date	12/31/2001
				First Named Inventor:	Sushma S. Trivedi, et al.
				Art Unit	2183
				Examiner Name	Huisman, David J.
				Attorney Docket Number	4860P2688
Sheet	1	of		1	
U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		us- 5,381,144	01-1995	Wilson et al.	
		us- 6,188,797	02-2001	Moledina et al.	
		us- 6,912,070	06-2005	Govindaswamy et al.	
		us- 7,055,018	05-30-2006	Bratt et al.	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		www.dictionary.com search terms: last, specify, host. Downloaded 08/12/2005, 5 pages.	
		MOLLOY, Stephen, et al., "Low Power VLSI Architectures for Variable-Length Encoding and Decoding" IEEE copyright 1997, p. 997-1000.	
		ROSENBERG, Jerry M., "Dictionary of Computers, Information Processing & Telecommunications" New York: John Wiley & Sons Copyright 1987 p. 275 and 455.	

Examiner Signature	Date Considered
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